

# Technical Bulletin

## Intel Puma 6 Silicon Change from “B-Step” to “D-Step”

**ATB Number:** ATB15-040

**Issue Date:** August 31, 2015

**Product Classification:** Hardware

**ATB Classification:** Class D

### Affected Product Models:

TM1602A/NA (782368)	TG1662A/NA (801187)	TG1672A/CE (801101)
TM1602G/NA-0 (782369)	TG1662A/CE (801186)	TG1672G/NA-0 (800947)
TM1602G/NA-8 (783946)	TG1662A/AR (781187)	TG1672G/NA-8 (800937)
TM1602G/NA-16 (783947)	TG1662A/BR (781188)	TG1672G/NT-0 (804033)
TM1602G/NA-85-0 (783992)	TG1662G/NA-0 (800944)	DG2460A/NA (804106)
TM1602G/NA-85-8 (783991)	TG1662G/NA-8 (800945)	DG2460A/CE (804095)
TM1602G/NA-85-16 (783990)	TG1662G/CE-0 (800943)	DG2470A/NA (783939)
DG1660A/NA (800941)	TG1662G/CE-8 (800942)	TG2472G/NA-0 (783937)
DG1660A/CE (800940)	TG1662S/CE (801185)	TG2472G/NA-8 (804032)
DG1670A/NA (800938)	TG1672A/NA (801100)	TG2472G/NA-85-0 (783938)

### Products Affected

Above TM / DG / TG16XX and DG / TG24XX Puma 6 Variants

### Reason for Change

Intel Puma-6 chipset revision change from production “B-Step” to a new “D-Step” revision is prompting ARRIS product re-certification with D-Step silicon. Intel is changing silicon revisions as part of its ongoing product support plans and roadmap.

### Description/Effect of Change

The Intel Puma 6 “D-Step” is pin-to-pin compatible and functionally equivalent with the “B-Step”. ARRIS products require only a firmware update to add support for the D-Step (in addition to the B-Step revision), so that product performance can be seamlessly sustained.

### Customer Action Required

Customers with deployed product from the above list should plan to deploy ARRIS TS 9.1.67 firmware (or later), which contains recognition for both B-Step and D-Step silicon revisions. This action must be taken in advance of deploying product with the D-Step silicon revision embedded. Customers are encouraged to take whatever re-certification steps are required to approve the later firmware revision for this transition to D-Step across all affected products.

### Implementation Time Frame

ARRIS will stage D-Step introduction on a per-model basis, so customers may expect to receive D-Step based product as early as Q1 2016. Contact ARRIS to identify the exact introduction schedule specific to CPE models in use by your organization.

### Additional Notes:

Extensive D-Step silicon testing has been performed by both ARRIS and Intel to ensure equivalence with the “B-Step” device. As long as customers have approved ARRIS TS 9.1.67 or later firmware, the need for re-certification is expected to be minimal.

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### Contact Technical Support

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+81-3-5461-7320	Japan

Note: Some operators require local markets to contact their own central/national technical support centers. Please follow your company's support escalation procedure before attempting to contact ARRIS directly.

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# Intel Puma 6 Silicon Change Notice from B-Step to D-Step

# Intel Puma 6 Silicon Change Notice from B-Step to D-Step on Touchstone products

- Reason for Change
  - Intel is transitioning its Puma 6 silicon from a “B-Step” to a newer “D-Step” variant as part of its normal ongoing product improvement program. Intel is subsequently pre-announcing the discontinuance of the older B-Step variant.
- Products Affected
  - All ARRIS Touchstone<sup>®</sup> TM/DG/TG16XX and DG/TG24XX Puma 6 Variants
- Description of Change
  - This new Intel Puma 6 D-Step device is pin-to-pin compatible to the existing B-Step device and functions the same.
  - A slight firmware change is required on existing ARRIS CPE models manufactured with the D-Step devices to a firmware release of 9.1.67 or later.

# Intel Puma 6 Silicon Change Notice from B-Step to D-Step on Touchstone products

- Customer Action Required
  - All sites that have deployed ARRIS 16XX and 24XX CPE product will need to be running a firmware release of 9.1.67 or later prior to adding new units which incorporate the D-Step silicon change.
- Implementation Time Frame
  - ARRIS will stage D-Step introduction on a per-model basis, so customers may expect to receive D-Step based product as early as Q3 2016.
  - Further announcements by ARRIS model number will be made as this time approaches.
- Equivalency Validation
  - Extensive D-Step silicon testing has been performed by both ARRIS and Intel to ensure equivalence with the “B-Step” device

# Intel Puma 6 Silicon Change Notice from B-Step to D-Step on Touchstone products

- Summary

- The transition to Puma 6 D-Step is part of a routine transition that occurs with most silicon devices as an outgrowth of ongoing product support plans.
- A firmware release of 9.1.67 or later is needed prior to adding new units which incorporate the D-Step silicon change.
- ARRIS will stage D-Step introduction on a per-model basis, so customers may expect to receive D-Step based product as early as Q3 2016.